
Short Information

CCZ 3005K

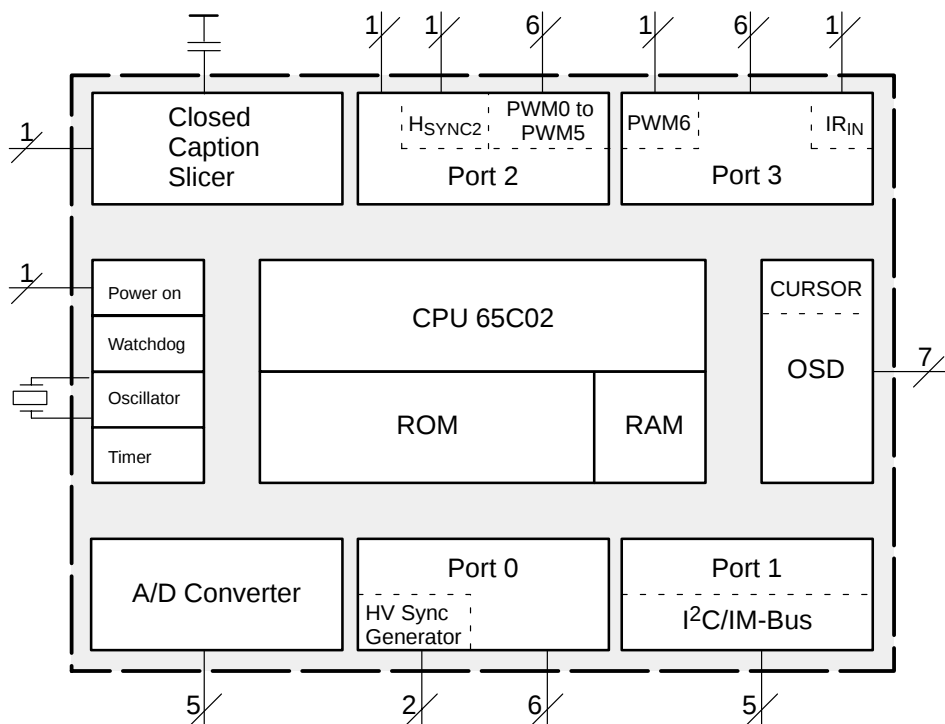
Central Control Unit

The Central Control Unit CCZ 3005K is an integrated circuit designed in CMOS technology and housed in a 52-pin Plastic Shrink Dual-In-Line Package (PSDIP). It is used as a single-chip TV controller with embedded Closed Caption Decoder and On-Screen Display.

Features:

- 6 MHz 65C02 CPU, 12 MHz crystal
- 52-pin PSDIP package
- on-chip oscillator
- clock generator with programmable frequency
- 62 KBytes internal ROM
- 1536 Bytes internal RAM
- Closed Caption Decoder
- programmable TV-line detector
- 2 selectable H_{SYNC} inputs
- 2 programmable H & V sync outputs
- free running H & V sync generator for stable OSD
- full-screen OSD with separate 24*24 pixel Cursor controls RGB and Fast Blank outputs
- color palette: 8 out of 64 different colors programmable
- soft-scroll, underline, flash, italics
- Half-Video control output
- I²C/IM-bus master interface
- six 6-bit D/A converters (PWM)
- single 14-bit D/A converter (PWM) for voltage synthesizer
- up to 29 port lines
- 8-bit A/D converter (6-bit precision) with 5 multiplexed inputs
- infrared input hardware supporting software decoding
- free-running timer generating interrupts
- power-on and clock supervision
- watchdog

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Block diagram of the CCZ 3005K

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